

SERIAL DATA TRANSFER DEVICE

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PURPOSE: To transfer data and confirm data transfer only with two signal lines, namely, a clock line and a data line by providing a master station as well as a slave station with a means which outputs a reception confirmation signal to the data line and a means which detects this signal.

CONSTITUTION: When a serial clock source flag 350 is set, a pertinent serial data processor 300 is the master station. An ACK control circuit 355 of the master station 300 has the function to confirm that the reception confirmation signal is outputted onto a serial data line 320 and the function to output the reception confirmation signal by driving the line 320 to the low level. A slave station 310 has the same constitution as the processor 300, and its ACK control circuit 365 has the function to output the reception confirmation signal to the line 320 similarly to the circuit 355 of the master station 300 and the function to detect the reception confirmation signal (in the low level) inputted through the line 320. Thus, data transmission and reception are confirmed by two lines, namely, the serial data line 320 and the serial clock line 321.

